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APPLICANTS

Ian Juso Dedic, Northolt, UNITED KINGDOM;

** CONTINUING DATA *****

This application is a DIV of 09/987,279 11/14/2001 PAT 6,628,219

** FOREIGN APPLICATIONS *****

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>		

ADDRESS

21171
 STAAS & HALSEY LLP
 SUITE 700
 1201 NEW YORK AVENUE, N.W.
 WASHINGTON , DC
 20005

TITLE

Reducing jitter in mixed-signal integrated circuit devices

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		<input type="checkbox"/> 1.16 Fees (Filing)
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